

Photoemission Spectroscopic Studies on Oxide/SiC Interfaces Formed by Dry and Pyrogenic Oxidation

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Abstract. The difference in the structure of oxide/SiC interface between dry and pyrogenic oxidation and the effect of post-oxidation annealing in Ar atmosphere on the interfaces have been studied by X-ray and ultraviolet photoelectron spectroscopy to make clear the interface structures which spoil the electrical properties. It is found that intermediate layers containing Si¹⁺ oxidation states exist in both cases of oxidation method and the thickness of the layer changes by Ar annealing. It is also found that the oxide/SiC band offsets and the structures of O2p peak, observed in valence band spectra, change remarkably by Ar POA in both cases of oxidation method. The bond states at SiC/oxide interfaces have been discussed by considering these results together with the results from C-V measurements.

Introduction

SiC MOSFETs have severe problems to be solved before practical use, such as their higher on-resistances than those predicted from bulk properties of SiC. It has been reported that the reason for this poor electrical property is due to the low channel mobility μ_{ch} in inversion layers of SiC MOSFETs,[1] and the small μ_{ch} resultes from the high interface state density D_{it} .[2] It is well known that the electrical properties of SiC MOS capacitors and MOSFETs depend strongly on the oxidation methods and post-oxidation processes.[3] A lot of photoemission spectroscopic studies have been carried out to make clear the structure of oxide/SiC interfaces.[4,5] We have reported from the results of x-ray photoelectron spectroscopy (XPS) measurements for slope shaped oxide films with various post-oxidation processes that several differences in photoemission peaks were observed.[6,7] However, so far, nothing can be specified as the origins which bring about poor electrical property such as high D_{it} . In our recent work, [8] it was found from the results of capacitance to gate-bias voltage (C-V) measurements that the flat-band voltage $V_{\rm fb}$ shifts toward higher gate-bias voltage side with increasing the sweep range of gate-bias voltage in the accumulation side, only when the oxidation is carried out under high purity oxygen flow. Besides, the magnitude of this shift, denoted as $\Delta V_{\rm fb}^{\rm dyn}$, decreases by post-oxidation annealing (POA) at the temperatures higher than 600 °C. Since the large $\Delta V_{\rm fb}^{\rm dyn}$ should also be a severe problem for the practical use of SiC MOS device, it is important to specify the interface structure which brings about the large $\Delta V_{\rm fb}^{\rm dyn}$.

In this report, the difference of oxide/SiC interface structures between dry and pyrogenic oxidation and the effect of post-oxidation annealing in Ar atmosphere (Ar POA) for the interfaces have been studied by X-ray and ultraviolet photoelectron spectroscopy (XPS and UPS) to make clear the interface structures which bring about the change of electrical properties. Also, the oxide/SiC band lineups were determined by the analysis of O1s energy loss spectra and the valence band spectra. We will discuss the interface structures and the band structures by considering the results of XPS and UPS together with those of C-V measurements for the samples oxidized by the same processes as were done for XPS and UPS.

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Experiments

Epitaxial wafers of 4H-SiC with 8° off-oriented (0001) Si-face, *n*-type, N_d - $N_a = 6X10^{15}$ cm⁻³ were used and four kinds of specimens were prepared in this study. After standard RCA cleaning, two specimens were oxidized up to around 50 nm in oxide thickness in pure dry O₂ flow at 1200 °C (denoted as sample (a) and sample (b)), and the other two specimens were oxidized up to the same thickness as samples (a) and (b) in mixed O₂ and H₂ flow (O₂:H₂ = 3:1) at 1200 °C (denoted as sample (c) and sample (d)). The samples (a) and (c) were quenched at room temperature after oxidation. The samples (b) and (d) were followed by annealing in Ar atmosphere for 3 hours at 950 °C. All the samples were etched off by buffered HF solution, resulting in the oxide thickness of ~ 1.3 nm, measured by a spectroscopic ellipsometer. XPS and UPS measurements were performed with non-monochromatized Mg K α line (=1253.6 eV) and He II line (= 40.8 eV) as a light source, respectively.

Results and Discussions

The XPS spectra in Si2p and C1s regions for samples (a)-(d) are shown in Figs. 1-i) and ii), respectively. The horizontal axes were calibrated by referring the SiC peak in C1s region (its binding energy = 282.7 eV), and the spectra were normalized by the corresponding SiC peak intensity of C1s. All the measured spectra were decomposed by Gaussian, taking into account Lorentzian contribution as well, as reported in Ref. [5]. Figures 1-iii) and iv) show the deconvolution results for sample (a) at $\theta_e = 90^\circ$, for example. Here, in-take angle of photoelectron is indicated by θ_e , which is regarded as an angle from the sample surface. Only the peaks corresponding to Si¹⁺ oxidation state and C-H bond in Si2p and C1s, respectively, were observed except those from Si-C bonds. The features of the spectra for samples (b)-(d) were similar to that for sample (a). The C-H peak observed is thought to be due to surface contamination because the C-H peak intensity increases with increasing the surface sensitivity of measurements, *i.e.* with decreasing θ_e . Therefore, only the peak corresponding to Si¹⁺ oxidation



Fig. 1. XPS spectra of the samples (a)-(d): i) Si2p at $\theta_e=90^\circ$, ii) C1s at $\theta_e=90^\circ$, iii) deconvolution of Si2p for sample (a), iv) deconvolution of C1s for sample (a).

state is observed at the interface. The Si¹⁺ state is considered to be due to the existence of C-Si*-O-Si bonds parallel to c-axis, where asterisk denoted as a Si termination layer in SiC bulk crystal. The intensity ratio Si¹⁺/Si⁰⁺ for samples (a)-(d) are listed in Tab. 1, where θ_e is selected for each sample so

as to cancel out the difference in oxide thickness between the samples. We found that the value of Si^{1+}/Si^{0+} decreases by Ar POA in the case of dry oxidation, while the value increases by Ar POA in the case of pyrogenic oxidation.

Figure 2 shows the O1s energy loss spectra for samples (a)-(d). The energy gaps E_g of interface layers were determined by the scheme reported in Ref. [9], as also shown in Tab. 1. The value of E_g for pyrogenic oxidation is larger than that for dry oxidation in both cases of quenched and Ar POA samples, and the values increased by Ar POA in both cases of oxidation method.

The UPS spectra for samples (a)-(d) at various θ_e are shown in Fig. 3. We can see two peaks in the spectra, large one around 5.5 eV and one at the higher energy side (~ 9.5 eV). It is considered that this

twin peak is originated from O2p, as reported in the case of oxide films on Si that the twin peak with the peaks at 7.0 and 10.5 eV below Si valence band maximum (VBM) arises in O2p region when the oxide thickness is around or less than 10 nm.[10] It has been also reported in ref.[10] that the peak at lower energy side and that at higher energy side of the O2p twin peak correspond to O2p non-bonding state and intermediate oxidation state, respectively. It is found from the figure that the intensity of the O2p twin peak decreases by Ar POA. This decrease can be explained by the reduction in the number of oxygen atoms at the interface. On the other hand, the difference in O2p peak intensities between two oxidation methods is observed in POA samples, but not in quenched samples. These results can be explained by the enhancement of oxygen removing in aid of hydrogen. Figure 3 shows that the changes of O2p intensity by Ar POA for both the oxidation methods become larger with decreasing θ_{e} . This suggests that the change of the interface structures by Ar POA is mainly arisen not at the oxide side but at the SiC side of the interface layer since the UPS measurement with smaller $\theta_{\rm P}$ is more surface-sensitive in general for amorphous material. The valence band offset ΔE_v was evaluated from the deconvolution of valence band spectra. Also, the conduction band offset ΔE_c was evaluated from the subtraction of ΔE_v from E_g of the interface layer, as listed in Tab. 1. It is found that the band offsets in both the oxidation methods change remarkably by Ar POA.

We have performed *C-V* measurements for the samples prepared by the same ways as those used in this experiment, *i.e.* samples (a)-(d). The large $\Delta V_{\rm fb}^{\rm dyn}$ was observed in the case of the sample (a) (dry+quench).[8] While, such a shift was not observed after Ar POA though the net number of



various θ_{e} .

interface trapped charges per unit area N_{it} slightly increased. In the case of pyrogenic oxidation, such a shift was not observed at all and the value of N_{it} was very small. The results obtained from *C-V*

measurements are also listed in Tab. 1. Compared between the cases of samples (a) with (c) and (b) with (d), it can be said that the sample having a low N_{it} has a large E_g value. However, in the relation between quenched and Ar POA samples, there is no correspondence. This disagreement results from the difference in the mechanism of the increase of E_g in the case of pyrogenic oxidations and Ar POA. The large V_{fb} and ΔV_{fb}^{dyn} values observed in sample (a) correspond well with the small ΔE_c and E_g values of the interface layer. The values of band offset change remarkably by Ar POA, while N_{it} increases a little. There is no correspondence between the value of Si¹⁺/Si⁰⁺ and any other experimental values.

The difference of pyrogenic oxidation from dry one is the existence of -H and -OH radicals. Therefore, a hydrogen-sensitive measurement should be carried out to clarify the hydrogen effects for the change of interface structure.

	(a)dry+quench	(b)dry+POA	(c)pyro+quench	(d)pyro+POA
oxide thickness (nm)	1.35	1.47	1.13	1.47
Si ¹⁺ / Si ⁰⁺ (Si ⁴⁺ / Si ⁰⁺)	0.37 (0.48)	0.29 (0.51)	0.25 (0.46)	0.34 (0.68)
E_g of oxide (eV)	7.6	8.0	7.9	8.2
$\Delta E_v / \Delta E_c (\mathrm{eV})$	3.8 / 0.8	2.7 / 2.4	3.8 / 1.1	3.0 / 2.6
$V_{\rm fb}$ / $\Delta V_{\rm fb}^{\rm dyn}$ (V)	16.5 / 10.8	3.5 / ~ 0	3.1 / 0.67	3.1 / ~ 0
$N_{\rm it}({\rm cm}^{-2})$	5.5×10^{12}	8.3X10 ¹²	$1.5 X 10^{12}$	2.2×10^{12}

Table 1. Experimental results from XPS/UPS for samples (a)-(d), as well as those from C-V measurements.

Summary

We have studied the difference of oxide/SiC interface structures between dry and pyrogenic oxidation and the effect of Ar POA by XPS/UPS measurements. It is found from these studies, as well as those from *C-V* measurements, that there exist intermediate layers containing Si^{1+} oxidation states at the interfaces, and there is no correspondence between the thickness of the layer and the value from electrical measurements. It is also found that the valence band structure and the band-offset change remarkably by Ar POA in both cases of oxidation method.

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